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UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Mitsuaki Hori et al.**

Group Art Unit: **2826**

Serial No.: **10/662,384**

Examiner: **Ahmed N. Sefer**

Filed: **September 16, 2003**

Confirmation No.: **2168**

For: **SEMICONDUCTOR DEVICE, MANUFACTURE AND EVALUATION
METHODS FOR SEMICONDUCTOR DEVICE, AND PROCESS
CONDITION EVALUATION METHOD**

Attorney Docket Number: **031111**
Customer Number: **38834**

RESPONSE UNDER 37 C.F.R. § 1.111

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Date: March 17, 2005

Sir:

In response to the Office Action dated December 17, 2004, Applicants amend the claims as follows and submit the following remarks.

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.